

Micro-code control signals / control-word

Num Control signal At the next clock cycle it will:

EEPROM 2

C23	CE	Enable the program counter (PC) to advance one position	128
C22	HLT	Stops the clock (and thus the program from running)	64
C21	PCI	Read contents for the PC from the bus	32
C20	PCO	Put content of the PC on the bus	16
C19	MAI	Read an address into MA from the bus	8
C18	MI	Read contents into memory (at address MA) from the bus	4
C17	MO	Put content of memory (address MA) on the bus	2
C16	IRI	Read contents into the instruction register (IR) from the bus	1

EEPROM 1

C15	EO	Put content of the ALU (Sigma Out) on the bus	128
C14	OI	Read contents for the Output register from the bus	64
C13	AI	Read contents for register A from the bus	32
C12	AO	Put content of register A on the bus	16
C11	BI	Read contents for register B from the bus	8
C10	BO	Put content of register B on the bus	4
C9	ALM	ALU Mode	2
C8	ALO	ALU function [0]	1

EEPROM 0

C7	AL1	ALU function [1]	128
C6	AL2	ALU function [2]	64
C5	AL3	ALU function [3]	32
C4	ALCI	ALU Carry In	16
C3	n3		8
C2	n2		4
C1	n1		2
C0	n0		1

Future expansion:

SCR	Step Counter Reset (go to next instruction, even when steps left)	???
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